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# **IC Layout Basics : A Practical Guide**





## Synopsis

MASTER IC LAYOUT WITHOUT AN ENGINEERING BACKGROUND! Tto new chip applications such as cell phones, personal digital assistants, and consumer electronics, electronic semiconductor usage has exploded, creating an unprecedented demand for technicians skilled in CMOS and bipolar design and layout. In IC LAYOUT BASICS, you get the same top-notch material utilized in IBMâ <sup>™</sup>s successful training courses. This essential primerbrings you up to speed on: \* Integrated circuit processes \* Layout techniques \* Fundamental device concepts \* Wafer processes Writing for technicians without an engineering degree , the authors present concepts from the ground up, building on the simple until the complex becomes crystal clear. Examples, self-tests, and sidebars reinforce the material and make it all quick and painless. For maximum retention, each chapter includes preview points, "motivation" boxes, and executive summaries.

#### **Book Information**

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## **Customer Reviews**

I bought this book to see what "competition" thinks about IC Layout subject. Here are my personal comments on IC Layout Basics book:1. The book represents a very simple but detailed explanation of all the basic structures required by an IC Layout Designer to understand if s/he wants to know "why" and "how" devices are built and used in the industry. From the basic transistor and up to inductors all the details are very clear explained. I will recommend this book to any IC Layout

Designer who wants to now more than just pushing polygons to achieve a circuit representation in mask layers.2. The book advertise herself as a book to be put near the bed and used as an easy reading. Sorry to disappoint you but once I started reading I could not sleep. From page to page, from concept to concept I've got more and more captivated by the style and vocabulary of the text. I finished the book in one week but my sleeping had to suffer. It is written in a very easy to read vocabulary, with some light jokes and with a fine touch to make you more curious from chapter to chapter. After more than 15 years in this profession I really enjoyed reading your book.Writing a book of my own, I know the size of the effort required and I would like to personally thank you for such a nice presentation.3. When I wrote my book "CMOS IC Layout Concepts, Methodologies and Tools" I thought that I covered all the basic information that an IC Layout Designer needs to become effective and efficient in doing his/her job. I thought that I cover all the basic training required from grade 1 to grade 12. Reading this book I found that Christopher and Judy Saint are covering much deeper than me, with a lot more process information, the knowledge needed from grade 1 through 4.

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